

Vishay Foil Resistors

RoHS

COMPLIANT

# Ultra High Precision Z-Foil Surface Mount Current Sensing Chip Resistors with TCR of ± 0.05 ppm/°C and Power Coefficient of 5 ppm at Rated Power



Any value at any tolerance available within resistance range

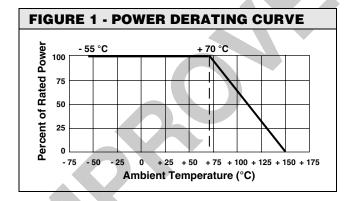
# INTRODUCTION

The Z-foil technology provides a significant reduction of the resistive component's sensitivity to ambient temperature variations (TCR) and applied power changes (PCR). Designers can now guarantee a high degree of stability and accuracy in fixed-resistor applications using solutions based on Vishay's revolutionary Z-foil technology.

Model VCS1625Z is a surface mount chip resistor designed with 4 pads for Kelvin connection. Utilizing Vishay's Bulk Metal<sup>®</sup> Z-foil as the resistance element, it provides performance capabilities far greater than other resistor technologies can supply in a product of comparable size. 0.05 ppm/°C absolute TCR removes errors due to temperature gradients.

This small device dissipates heat almost entirely through the pads so surface mount users are encouraged to be generous with the board's pads and traces.

Our application engineering department is available to advise and to make recommendations. For non-standard technical requirements and special applications, please contact us.



# **FEATURES**

- Temperature coefficient of resistance (TCR):
   ± 0.05 ppm/°C typical (0 °C to + 60 °C)
  - $\pm$  0.2 ppm/°C typical (- 55 °C to + 125 °C,
  - + 25 °C ref.) (see table 1)
- Tolerance: to ± 0.2 %
- Power coefficient "∆R due to self heating":
   5 ppm at rated power
- Load life stability: 0.02 % at 70 °C, 2000 h at rated power
- Electrostatic discharge (ESD) above 25 000 V
- Short time overload < 0.005 %</li>
- Ohmic values:  $0.3 \Omega$  to  $10 \Omega$  (for higher or lower values please contact us)
- Power rating: 1 W at + 70 °C (Figure 1)
- Non inductive, non capacitive design
- Rise time: 1 ns without ringing
- Current rating: 5 A maximum
- Current noise: < 40 dB
- Voltage coefficient: < 0.1 ppm/V</li>
- Non inductive: < 0.08 μH
- Non hot spot design
- Any value available within resistance range (e.g. 1R234)
- Prototype samples available from 48 h. For more information, please contact foil@vishav.com
- For better performances please contact us

# **TERMINATIONS**

- Two lead (Pb)-free options are available: gold plated or tin plated
- Tin/lead plated

# **APPLICATIONS**

- Military
- Medical
- Automatic test equipment (ATE)
- Airborne (in heads-up display systems)
- High precision instrumentation
- Electron beam recording equipment
- Electron microscopes
- Current sensing applications
- Forced balance electronic scales
- · Applications that require superior frequency stability

TABLE 1 - SPECIFICATIONS								
MODEL NUMBER	RESISTANCE RANGE	RESISTANCE TOLERANCE	POWER RATING at + 70 °C	TYPICAL TCR and MAX. SPREAD (- 55 °C to + 125 °C, + 25 °C)	MAXIMUM CURRENT			
VCS1625Z	$> 2.0 \Omega$ to 10 $\Omega$ 0.3 $\Omega$ to 2.0 $\Omega$	± 0.2 %, ± 0.5 %; ± 1.0 % ± 0.5 %; ± 1.0 %	1 W on alumina substrate (0.5 W on FR4 PCB)	± 0.2 ± 2.8 ppm/°C	5 A			

## Note

- Tighter performances are available. Please contact application engineering foil@vishay.com
- \* Pb containing materials are not RoHS compliant, exemptions may apply

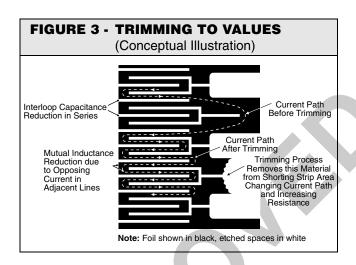
# VCS1625Z (Z-Foil)

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FIGURE 2 - DIMENSIONS in inches (millimeters)									
-  -	L W - W - Top View	Mounting A Pads (4) E <sub>1</sub> B  Bottom View	Solder Pad Layout    0.070	Electrical Schematic  I, E,  R }					
	IN	ICHES	MILLIMETER	s					
L	0.250 ± 0.010		6.35 ± 0.25	6.35 ± 0.25					
Н	0.160 ± 0.010		4.06 ± 0.25	4.06 ± 0.25					
W	0.040	maximum	1.02 maximu	1.02 maximum					
Α	0.08	0 ± 0.005	2.03 ± 0.13	2.03 ± 0.13					
В	0.04	0 ± 0.010	1.02 ± 0.25	1.02 ± 0.25					



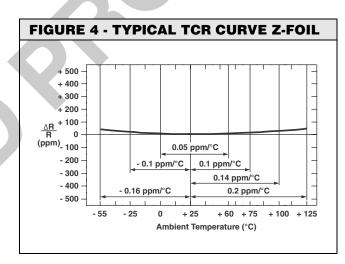


TABLE 2 - PERFORMANCE SPECIFICATIONS					
TEST	MIL-PRF-55342 ∆R LIMITS	TYPICAL ∆R LIMITS	MAXIMUM ∆R LIMITS¹)		
Thermal shock 5 x (- 65 °C to + 150 °C)	± 0.10 %	± 0.005 % (50 ppm)	± 0.01 % (100 ppm)		
Low temperature operation	± 0.10 %	± 0.005 % (50 ppm)	± 0.01 % (100 ppm)		
Short time overload	± 0.10 %	± 0.005 % (50 ppm)	± 0.02 % (200 ppm)		
High temperature exposure	± 0.10 %	± 0.01 % (100 ppm)	± 0.02 % (200 ppm)		
Resistance to soldering heat	± 0.2 %	± 0.01 % (100 ppm)	± 0.03 % (300 ppm)		
Moisture resistance	± 0.2 %	± 0.01 % (100 ppm)	± 0.03 % (300 ppm)		
Load life 2000 h at 70 °C: rated power on ceramic PCB	± 0.5 %	± 0.02 % (200 ppm)	± 0.04 % (400 ppm)		

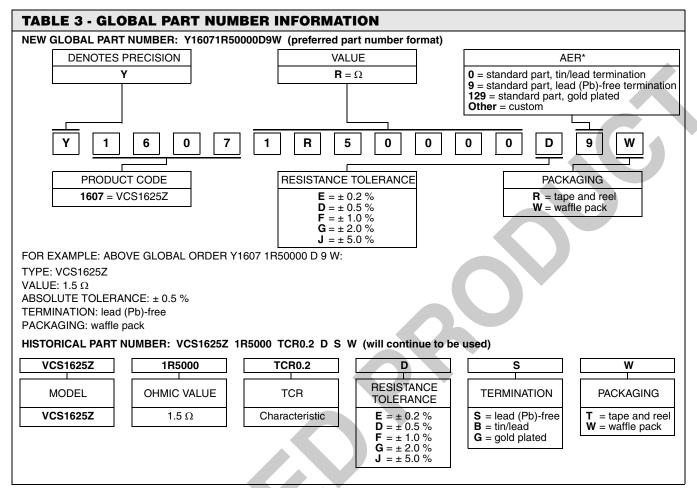
## Note

1. Measurement error 0.001 R



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## Note

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<sup>\*</sup> Application engineering release: for non-standard requests, please contact application engineering.



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